Abstract of the Disclosure

MULTIPLE PROCESSOR CORE DEVICE HAVING
SHAREABLE FUNCTIONAL UNITS FOR SELF-REPAIRING CAPABILITY

Multiple processor cores are implemented on a single integrated circuit chip, each having its own respective shareable functional units, which are preferably floating point units. A failure of a shareable unit in one processor causes that processor to share the corresponding unit in another processor on the same chip. Preferably, a functional unit is shared on a cycle interleaved basis.